

SECTION-B

11. Generate AND, OR and NOT gates using NAND gates.
12. a) Discuss the working of Full Adder Circuit.
b) What is meant by encoder? Explain.
13. What is meant by JK Flip Flop? Explain the race-around condition in detail.
14. Explain the Von Neumann Architecture.
15. a) What are memory reference instructions? Explain.
b) Explain the working of T-Flip Flop.
16. Draw and explain the working of 16 bit common bus system.

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